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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/664,547	09/19/2003	Theodore W. Houston	TI-32205.1	4290
	7590 02/26/200 RUMENTS INCORPO	EXAMINER		
POBOX 6554		LEWIS, MONICA		
DALLAS, TX 75265			ART UNIT	PAPER NUMBER
			2822	
			NOTIFICATION DATE	DELIVERY MODE
			02/26/2008	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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	Application No.	Applicant(s)	
	10/664,547	HOUSTON, THEODORE W.	
Office Action Summary	Examiner	Art Unit	
	MONICA LEWIS	2822	
The MAILING DATE of this communication ap Period for Reply	ppears on the cover sheet with the	correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPUBLICHEVER IS LONGER, FROM THE MAILING IF Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory perior Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATIO 1.136(a). In no event, however, may a reply be tild d will apply and will expire SIX (6) MONTHS from the, cause the application to become ABANDONE	N. mely filed the mailing date of this communication. ED (35 U.S.C. § 133).	
Status			
Responsive to communication(s) filed on 29 and 2a. This action is FINAL . Since this application is in condition for allow closed in accordance with the practice under	is action is non-final. ance except for formal matters, pr		
Disposition of Claims			
4) Claim(s) 49-56 is/are pending in the applicating 4a) Of the above claim(s) 55 and 56 is/are with 5) Claim(s) is/are allowed. 6) Claim(s) 49-54 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/off are specification is objected to by the Examing 10) The drawing(s) filed on 21 December 2003 is/Applicant may not request that any objection to the	thdrawn from consideration. /or election requirement. ner. /are: a)⊠ accepted or b)⊡ objec	•	
Replacement drawing sheet(s) including the corre		•	
Priority under 35 U.S.C. § 119	Examiner. Note the attached office	Action of format 10-132.	
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of: 1. Certified copies of the priority documents. Certified copies of the priority documents. Copies of the certified copies of the priority documents. * See the attached detailed Office action for a list	nts have been received. nts have been received in Applicat fority documents have been receiv au (PCT Rule 17.2(a)).	ion No ed in this National Stage	
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal R 6) Other:		

Application/Control Number: 10/664,547 Page 2

Art Unit: 2822

DETAILED ACTION

1. This office action is in response to the amendment filed November 29, 2007.

Continued Examination Under 37 CFR 1.114

2. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 11/29/07 has been entered.

Response to Arguments

3. Applicant's arguments with respect to claims 49-56 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 112

- 4. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 5. Claim 55 recites the limitation "premetal dielectric." There is insufficient antecedent basis for this limitation in the claim.

Election/Restrictions

6. Newly submitted claims 55 and 56 are directed to an invention that is independent or distinct from the invention originally claimed for the following reasons: a) they are directed to a different embodiment.

Since applicant has received an action on the merits for the originally presented invention, this invention has been constructively elected by original presentation for prosecution

Application/Control Number: 10/664,547 Page 3

Art Unit: 2822

on the merits. Accordingly, claims 55 and 56 are withdrawn from consideration as being directed to a non-elected invention. See 37 CFR 1.142(b) and MPEP § 821.03.

Claim Rejections - 35 USC § 103

- 7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 8. Claims 49-52 and 54 are rejected under 35 U.S.C. 103(a) as obvious over Turner (U.S. Patent No. 5,357,132) in view of Kurosawa et al. (U.S. Patent No. 4,951,175).

In regards to claim 49, Turner discloses the following:

- a) a first insulating layer (66) disposed between a substrate and a first metal layer (68) (For Example: See Figure 9);
- b) a trench defined by a recess in the first insulating layer (For Example: See Figure 9);
- c) a first contact pillar (64) extending substantially from a top surface of the substrate to a bottom surface of the first metal layer (68) within the trench (For Example: See Figure 9); and
- d) a capacitor formed in the trench overlying the first contact pillar such that the capacitor is formed at least in part on a side of the first contact pillar, and the first contact pillar is a plate of the capacitor (For Example: See Column 6 Lines 46-50).

In regards to claim 49, Turner fails to disclose the following:

a) the trench does not extend beyond the top surface of the substrate.

However, Kurosawa et al. ("Kurosawa") discloses that the trench does not extend beyond the top surface of the substrate (For Example: See Figure 2). It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the semiconductor

device of Turner to include a trench that does not extend beyond the top surface of the substrate as disclosed Kurosawa in because it aids in providing high integration density (For Example: See Column 2 Lines 1-8).

Additionally, since Turner and Kurosawa are both from the same field of endeavor (semiconductors), the purpose disclosed by Kurosawa would have been recognized in the pertinent art of Turner.

In regards to claim 50, Turner discloses the following:

a) a second contact pillar (62) extending substantially from a top surface of the substrate to a bottom surface of another portion of the first metal layer wherein the second contact pillar is substantially the same height as the first contact pillar (For Example: See Figure 9).

In regards to claim 51, Turner discloses the following:

a) the capacitor comprises a storage element of a memory cell (For Example: See Column 1 Lines 15-25).

In regards to claim 52, Turner discloses the following:

a) a storage node of the storage element comprises a first contact pillar (For Example: See Abstract).

In regards to claim 54, Turner discloses the following:

- a) the second contact pillar is a bit line contact pillar (76 and 78) (For Example: See Figure 11).
- 9. Claim 53 is rejected under 35 U.S.C. 103(a) as obvious over Turner (U.S. Patent No. 5,357,132) in view of Kurosawa et al. (U.S. Patent No. 4,951,175) and Fisher et al. (U.S. Patent No. 5,962,885).

In regards to claim 53, Turner fails to disclose the following:

a) a storage node further comprises a conducting layer lining the trench and the side of the first contact pillar.

Application/Control Number: 10/664,547 Page 5

Art Unit: 2822

However, Fisher et al. ("Fisher") discloses the use of a storage node further that

comprises a conducting layer (80) lining the trench and the side of the first contact pillar (70)

(For Example: See Figure 6). It would have been obvious to one having ordinary skill in the art

at the time the invention was made to modify the semiconductor device of Turner to include

storage node that comprises a conducting layer lining the trench and the side of the first contact

pillar as disclosed in Fisher because it aids in increasing capacitance (For Example: See

Column 1 Lines 51 and 52).

Additionally, since Turner and Fisher are both from the same field of endeavor

(semiconductors), the purpose disclosed by Fisher would have been recognized in the pertinent

art of Turner.

Conclusion

10. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to MONICA LEWIS whose telephone number is 571-272-1838.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor,

Zandra Smith can be reached on 571-272-2429. The fax phone number for the organization

where this application or proceeding is assigned is 571-273-8300 for regular and after final

communications.

/Monica Lewis/

Primary Examiner, Art Unit 2822

February 25, 2008